

Procedure. Ref.: SVT_OPM_5800 (1,1) HIGH VOLTAGE INTERFACE TEST

Procedure Title: High Voltage Interface Verification.

Last Date Modified: 16/07/99 11:51:29

Author: Kate Adamson

Purpose of Procedure:

This procedure executes a functional test of the OM Electronic interface in Engineering Mode. The High Voltage Unit is partially switched ON. Note: The levels at which the HVU is operated are not sufficient to permit light amplification and so the darkness of the working environment is not critical.

A full Frame Image is acquired and dumped. The Filter Wheel is commanded to the Blocked position and the event detection threshold is set to "2" to generate systematic noise events.

This procedure to be executed during SVT ONLY with the support of MSSL staff.

Procedure is extracted from the XMM-OM - Integrated Functional Test Procedures XMM-OM/MSSL/SP/0194.8 Procedure 5.10.

Initial State: OM ON in an Operational Mode. SAFE, IDLE, ENG or SCIENCE. OM secondary rails are ON.

Final State: OM ON in ENGINEERING Mode.

Step	Time	Activity/Remarks	Command	TM Verification
1		Confirm OM is in one of the operational modes. IDLE, SAFE, INTER SAFE, SCIENCE or ENGINEERING Confirm Secondary Rails are ON.		<u>AND: H100</u> H5395 "OM STATE" = H5280 "CURRENT +25 SV" = 15 to 22 mA H5285 "CURRENT +15 SV" = 39 to 54 mA H5290 "CURRENT +11 SV" = 87 to 96 mA H5295 "CURRENT +5.3 SV" = 393 to 541 mA H5300 "CURRENT -5.3 SV" = 368 to 506 mA H5305 "CURRENT -15 SV" = 43 to 60 mA
2		COMPLETE DPU RESET		
2.1		Reset DPU. Executes a Hardware reset of the DSPs	<u>H7201</u> RESET DSP	<u>AND: H100</u> H5395 "OM STATE" = SAFE H5450 "DPU STATE" = BOOT
2.2	00:00:12	Wait up to 12 s for the DA_DPU_BOOT_READY (aka Fred from the Dead) Packet 92201 (PK Dump 1145 OM4)		<u>AND: H110</u> H5450 "DPU STATE" = BOOT H5455 "BLUE 1 DSP STAT" = OFF H5460 "BLUE 2 DSP STAT" = OFF H5465 "RED DSP STAT" = OFF H5470 "BLUE 1 DSP DC" = H5475 "BLUE 2 DSP DC" = H5365 "TRACKING" = H5480 "SWAP UNIT ID" = H5485 "DPU TASK ID" = H7675 "EXPOSURE NUMBER" = H7680 "TIMESTAMP" =
2.3		Switch to the DPU Loadable Code (DPUOS). This TC instructs the DPU to load the DPUOS	<u>H7202</u> LOAD DPUOS	

Procedure. Ref.: SVT_OPM_5800 (1,1) HIGH VOLTAGE INTERFACE TEST

Step	Time	Activity/Remarks	Command	TM Verification
2.4	00:00:10	Wait for up to 10 seconds for the DA_DPUOS_READY Data alert. - aka "Jim from the Rim" Packet 92202 (PK Dump 1146 OM4)		<u>AND: H110</u> H5450 "DPU STATE" = DPUOS H5455 "BLUE 1 DSP STAT" = H5460 "BLUE 2 DSP STAT" = H5465 "RED DSP STAT" = H5470 "BLUE 1 DSP DC" = H5475 "BLUE 2 DSP DC" = H5365 "TRACKING" = H5480 "SWAP UNIT ID" = H5485 "DPU TASK ID" =
2.5		Enable BLUE 1 DSP (Digital Signal Processor)	<u>H7207</u> ENABLE DSP **H0522 "COLOURED DSP" = BLUE 1 **H0016 "ENABLE CNTL" = ENABLED	
2.6	00:00:10	Wait for up to 10 seconds. Check TM status		<u>AND: H110</u> H5450 "DPU STATE" = DPUOS H5455 "BLUE 1 DSP STAT" = ON H5460 "BLUE 2 DSP STAT" = OFF H5465 "RED DSP STAT" = OFF
2.7		Enable BLUE 2 DSP (Digital Signal Processor)	<u>H7207</u> ENABLE DSP **H0522 "COLOURED DSP" = BLUE 2 **H0016 "ENABLE CNTL" = ENABLED	
2.8	00:00:10	Wait for up to 10 seconds. Check TM status		<u>AND: H110</u> H5450 "DPU STATE" = DPUOS H5455 "BLUE 1 DSP STAT" = ON H5460 "BLUE 2 DSP STAT" = ON H5465 "RED DSP STAT" = OFF
2.9		Enable RED DSP (Digital Signal Processor)	<u>H7207</u> ENABLE DSP **H0522 "COLOURED DSP" = RED **H0016 "ENABLE CNTL" = ENABLED	
2.10	00:00:10	Wait for up to 10 seconds. Check TM status		<u>AND: H110</u> H5450 "DPU STATE" = DPUOS H5455 "BLUE 1 DSP STAT" = ON H5460 "BLUE 2 DSP STAT" = ON H5465 "RED DSP STAT" = ON
2.11		Initialise DPU IC_INIT_DPU Zeroes memory, readies the swap units.	<u>H7248</u> INIT DPU	
2.12		Wait for up to 25s for the DA_EOT_INIT_DPU event to confirm completion of the DPU initialisation. (Dave from the Grave) Packet 92210 (PK Dump 1154 OM4)		<u>AND: H910</u> H7680 "TIMESTAMP" =
3		PREPARE DPU FOR IMAGE ACQUISITION		
3.1		Command OM to IDLE MODE (if not already in IDLE Mode)	<u>H9002</u> GOTO IDLE	<u>AND: H100</u> H5395 "OM STATE" = IDLE
3.2		Set Exposure Number	<u>H7238</u> SET EXPOSURE ID **H0530 "EXPOSURE ID" = 2	

Procedure. Ref.: SVT_OPM_5800 (1,1) HIGH VOLTAGE INTERFACE TEST

Step	Time	Activity/Remarks	Command	TM Verification
4		PREPARE BPE FOR IMAGE ACQUISITION		
4.1		Set Acquisition Mode Hi Res Full Frame	H7130 LOAD ACQ MODE **H0110 "ACQ MODE" = HI RES FULL	AND: H110 H5215 "ACQUISITION MODE" = HI RES FULL
4.2		Set Event Threshold	H7131 SET EVT THRESH **H0120 "THRESHOLD" = 2	AND: H110 H5235 "EVT DET THRESH" = 2
4.3		LOAD CENTROID CHANNEL BOUNDARIES		
4.4		Load the Centroid Channel Boundaries	H7100 LOAD CENTRD TAB **H0010 "VERIFICATION CTL" = ENABLED **H0020 "X0 TAB BOUNDARY" = -1000 **H0021 "X1 TAB BOUNDARY" = -750 **H0022 "X2 TAB BOUNDARY" = -500 **H0023 "X3 TAB BOUNDARY" = -250 **H0024 "X4 TAB BOUNDARY" = 0 **H0025 "X5 TAB BOUNDARY" = 250 **H0026 "X6 TAB BOUNDARY" = 500 **H0027 "X7 TAB BOUNDARY" = 750 **H0028 "X8 TAB BOUNDARY" = 1000 **H0030 "Y0 TAB BOUNDARY" = -1000 **H0031 "Y1 TAB BOUNDARY" = -750 **H0032 "Y2 TAB BOUNDARY" = -500 **H0033 "Y3 TAB BOUNDARY" = -250 **H0034 "Y4 TAB BOUNDARY" = 0 **H0035 "Y5 TAB BOUNDARY" = 250 **H0036 "Y6 TAB BOUNDARY" = 500 **H0037 "Y7 TAB BOUNDARY" = 750 **H0038 "Y8 TAB BOUNDARY" = 1000	
4.5		Start the Load Centroid Channel Boudaries Task	H5100 LOAD CENTRD TAB	
4.6	00:02:30	Wait for up to 2.5 minutes for the Detector Event. Packet 92100 (PK Dump 1016 OM4)		AND: H911 HU700 "DETECTOR EVENT" = CEN TAB LOAD
4.7		LOAD WINDOW PARAMETERS CHECK IF PRIME OR REDUNDANT OM IS USED.		

Procedure. Ref.: SVT_OPM_5800 (1,1) HIGH VOLTAGE INTERFACE TEST

Step	Time	Activity/Remarks	Command	TM Verification
4.8		IF PRIME OM - CONTINUE Else Go to Next Step. Load the Window Parameters PRIME	H7110 LOAD WINDOW TAB **H0010 "VERIFICATION CTL" = ENABLED **H0040 "NO OF WINDOWS" = 1 **H0050 "XLOW WINDOW 1" = 52 **H0051 "YLOW WINDOW 1" = 27 **H0052 "XSIZE WINDOW 1" = 256 **H0053 "YSIZE WINDOW 1" = 256 H0054 "XLOW WINDOW 2" = 0 H0055 "YLOW WINDOW 2" = 0 H0056 "XSIZE WINDOW 2" = 0 H0057 "YSIZE WINDOW 2" = 0 H0058 "XLOW WINDOW 3" = 0 H0059 "YLOW WINDOW 3" = 0 H0060 "XSIZE WINDOW 3" = 0 H0061 "YSIZE WINDOW 3" = 0 H0062 "XLOW WINDOW 4" = 0 H0063 "YLOW WINDOW 4" = 0 H0064 "XSIZE WINDOW 4" = 0 H0065 "YSIZE WINDOW 4" = 0 H0066 "XLOW WINDOW 5" = 0 H0067 "YLOW WINDOW 5" = 0 H0068 "XSIZE WINDOW 5" = 0 H0069 "YSIZE WINDOW 5" = 0 H0070 "XLOW WINDOW 6" = 0 H0071 "YLOW WINDOW 6" = 0 H0072 "XSIZE WINDOW 6" = 0 H0073 "YSIZE WINDOW 6" = 0 H0074 "XLOW WINDOW 7" = 0 H0075 "YLOW WINDOW 7" = 0 H0076 "XSIZE WINDOW 7" = 0 H0077 "YSIZE WINDOW 7" = 0 H0078 "XLOW WINDOW 8" = 0 H0079 "YLOW WINDOW 8" = 0 H0080 "XSIZE WINDOW 8" = 0 H0081 "YSIZE WINDOW 8" = 0 H0082 "XLOW WINDOW 9" = 0 H0083 "YLOW WINDOW 9" = 0 H0084 "XSIZE WINDOW 9" = 0 H0085 "YSIZE WINDOW 9" = 0 H0086 "XLOW WINDOW 10" = 0 H0087 "YLOW WINDOW 10" = 0 H0088 "XSIZE WINDOW 10" = 0 H0089 "YSIZE WINDOW 10" = 0 H0090 "XLOW WINDOW 11" = 0 H0091 "YLOW WINDOW 11" = 0 H0092 "XSIZE WINDOW 11" = 0 H0093 "YSIZE WINDOW 11" = 0 H0094 "XLOW WINDOW 12" = 0 H0095 "YLOW WINDOW 12" = 0 H0096 "XSIZE WINDOW 12" = 0 H0097 "YSIZE WINDOW 12" = 0 H0098 "XLOW WINDOW 13" = 0 H0099 "YLOW WINDOW 13" = 0 H0100 "XSIZE WINDOW 13" = 0 H0101 "YSIZE WINDOW 13" = 0 H0102 "XLOW WINDOW 14" = 0 H0103 "YLOW WINDOW 14" = 0 H0104 "XSIZE WINDOW 14" = 0 H0105 "YSIZE WINDOW 14" = 0 H0106 "XLOW WINDOW 15" = 0 H0107 "YLOW WINDOW 15" = 0 H0108 "XSIZE WINDOW 15" = 0 H0109 "YSIZE WINDOW 15" = 0	

Procedure. Ref.: SVT_OPM_5800 (1,1) HIGH VOLTAGE INTERFACE TEST

Step	Time	Activity/Remarks	Command	TM Verification
4.9		IF REDUNDNAT OM: Load the Window Parameters REDUNDANT	H7110 LOAD WINDOW TAB **H0010 "VERIFICATION CTL" = ENABLED **H0040 "NO OF WINDOWS" = 1 **H0050 "XLOW WINDOW 1" = 64 **H0051 "YLOW WINDOW 1" = 15 **H0052 "XSIZE WINDOW 1" = 256 **H0053 "YSIZE WINDOW 1" = 256 H0054 "XLOW WINDOW 2" = 0 H0055 "YLOW WINDOW 2" = 0 H0056 "XSIZE WINDOW 2" = 0 H0057 "YSIZE WINDOW 2" = 0 H0058 "XLOW WINDOW 3" = 0 H0059 "YLOW WINDOW 3" = 0 H0060 "XSIZE WINDOW 3" = 0 H0061 "YSIZE WINDOW 3" = 0 H0062 "XLOW WINDOW 4" = 0 H0063 "YLOW WINDOW 4" = 0 H0064 "XSIZE WINDOW 4" = 0 H0065 "YSIZE WINDOW 4" = 0 H0066 "XLOW WINDOW 5" = 0 H0067 "YLOW WINDOW 5" = 0 H0068 "XSIZE WINDOW 5" = 0 H0069 "YSIZE WINDOW 5" = 0 H0070 "XLOW WINDOW 6" = 0 H0071 "YLOW WINDOW 6" = 0 H0072 "XSIZE WINDOW 6" = 0 H0073 "YSIZE WINDOW 6" = 0 H0074 "XLOW WINDOW 7" = 0 H0075 "YLOW WINDOW 7" = 0 H0076 "XSIZE WINDOW 7" = 0 H0077 "YSIZE WINDOW 7" = 0 H0078 "XLOW WINDOW 8" = 0 H0079 "YLOW WINDOW 8" = 0 H0080 "XSIZE WINDOW 8" = 0 H0081 "YSIZE WINDOW 8" = 0 H0082 "XLOW WINDOW 9" = 0 H0083 "YLOW WINDOW 9" = 0 H0084 "XSIZE WINDOW 9" = 0 H0085 "YSIZE WINDOW 9" = 0 H0086 "XLOW WINDOW 10" = 0 H0087 "YLOW WINDOW 10" = 0 H0088 "XSIZE WINDOW 10" = 0 H0089 "YSIZE WINDOW 10" = 0 H0090 "XLOW WINDOW 11" = 0 H0091 "YLOW WINDOW 11" = 0 H0092 "XSIZE WINDOW 11" = 0 H0093 "YSIZE WINDOW 11" = 0 H0094 "XLOW WINDOW 12" = 0 H0095 "YLOW WINDOW 12" = 0 H0096 "XSIZE WINDOW 12" = 0 H0097 "YSIZE WINDOW 12" = 0 H0098 "XLOW WINDOW 13" = 0 H0099 "YLOW WINDOW 13" = 0 H0100 "XSIZE WINDOW 13" = 0 H0101 "YSIZE WINDOW 13" = 0 H0102 "XLOW WINDOW 14" = 0 H0103 "YLOW WINDOW 14" = 0 H0104 "XSIZE WINDOW 14" = 0 H0105 "YSIZE WINDOW 14" = 0 H0106 "XLOW WINDOW 15" = 0 H0107 "YLOW WINDOW 15" = 0 H0108 "XSIZE WINDOW 15" = 0 H0109 "YSIZE WINDOW 15" = 0	
4.10		Start the Load Window Table Task	H5110 LOAD WINDOW TAB	

Procedure. Ref.: SVT_OPM_5800 (1,1) HIGH VOLTAGE INTERFACE TEST

Step	Time	Activity/Remarks	Command	TM Verification
4.11	00:01:00	Wait for up to 1 minutes for the Detector Event. Packet 92100 (PK Dump 1016 OM4)		<u>AND: H911</u> HU700 "DETECTOR EVENT" = WIN TAB LOAD
4.12		Confirm Frame Tags are disabled.	<u>H7135</u> SET FRAME TAG **H0016 "ENABLE CNTL" = DISABLED	<u>AND: H110</u> H5220 "FRAME TAG CNTL" = DISABLED
5		SET FILTER WHEEL COARSE AND FINE LEDs TO REQUIRED VALUES.		
5.1		Set Coarse Sensor LED to 56 mA	<u>H7600</u> SET COARSE LED **H0300 "COARSE POS LED C" = 56	<u>AND: H110</u> H5245 "CPOS LED CURRENT" = 56 mA
5.2		Set Fine Sensor LED to 25 mA	<u>H7601</u> SET FINE LED **H0310 "FINE POS LED C" = 25	<u>AND: H110</u> H5205 "FPOS LED CURRENT" = 25 mA
6		PLACE FILTER WHEEL IN DATUM THEN BLOCKED		
6.1		SPECIFY NEXT MOVE OF THE FILTER WHEEL. Specify Filter wheel move to Datum position.	<u>H7608</u> SET FW TO DATUM	
6.2		MOVE FILTER WHEEL Move Filter Wheel to the specified position.	<u>H5600</u> MOVE FW	
6.3	00:00:25	WAIT FOR TM CONFIRMATION. Wait up to 25 seconds for the TMPSU event. Confirm receipt of TM packet 92600 (PK Dump 1096 OM4) NOTE - HK TM WILL BE SUSPENDED FOR THE DURATION OF THE FW MOVE.		<u>AND: H911</u> HU010 "TMPSU EVENT" = FW @ REQ POS
6.4		CONFIRM STATUS IN HK TM. When HK TM is resumed check FW status.		<u>AND: H110</u> H5150 "FINE PICKOFF" = SEEN H5265 "FW POS COUNTER" = 0
6.5		SPECIFY NEXT MOVE OF THE FILTER WHEEL. Specify Filter wheel move to BLOCKED position.	<u>H7604</u> SET FW NUMBER **H0280 "FW NUMBER" = BLOCKED	
6.6		MOVE FILTER WHEEL Move Filter Wheel to the specified position.	<u>H5600</u> MOVE FW	
6.7	00:00:25	WAIT FOR TM CONFIRMATION. Wait up to 25 seconds for the TMPSU event. Confirm receipt of TM packet 92600 (PK Dump 1096 OM4) NOTE - HK TM WILL BE SUSPENDED FOR THE DURATION OF THE FW MOVE.		<u>AND: H911</u> HU010 "TMPSU EVENT" = FW @ REQ POS

Procedure. Ref.: SVT_OPM_5800 (1,1) HIGH VOLTAGE INTERFACE TEST

Step	Time	Activity/Remarks	Command	TM Verification
6.8		CONFIRM STATUS IN HK TM. When HK TM is resumed check FW status.		<u>AND: H110</u> H5150 "FINE PICKOFF" = SEEN H5265 "FW POS COUNTER" = 1200
7		SETUP HIGH VOLTAGES FOR HV CHAIN. (PRIME AND REDUNDANT HV CHAINS TAKE THE SAME VALUES IN THIS PROCEDURE)		
7.1		Specify the Vmcp23 ramp parameters.	<u>H7140</u> SET HV RAMP PARA H0150 "VOLTAGE" = MCP23 **H0160 "TARGET VALUE" = 50 V H0175 "FORCE RAMP" = OFF	
7.2		Perform Ramp. Monitor the rise in the VMCP 23 voltage to 50V.	<u>H5140</u> START HV RAMP	<u>AND: H100</u> H5160 "HVM VMCP23" = 50 V
7.3	00:00:20	Wait up to 20 seconds for the confirmation of succesful ramp. Packet 92100 (PK Dump 1016 OM4)		<u>AND: H911</u> HU700 "DETECTOR EVENT" = MCP23 OK
8		ACQUIRE IMAGE		
8.1		Change to ENGINEERING Mode.	<u>H9004</u> GOTO ENG	<u>AND: H100</u> H5395 "OM STATE" = ENGINEERING
8.2		Turn on the Flood LEDs to Level 3 (4uA)	<u>H7134</u> SET FLOOD LED **H0140 "LED CURRENT" = 4 UAMP	<u>AND: H100</u> H5195 "FLOOD LED BIAS" = 4.0
8.3		Set the DPU Frame Time (in units of 1/1024) to allow for a 293 second exposure. DPU Frame Time = 300 000	<u>H7236</u> SET FRAME TIME **H0710 "NUM DPU CYCLES" = 300000 msec	
8.4		Start Sending Events to the DPU	<u>H5130</u> START DET INT	
8.5		START EXPOSURE - FULL HI RES Start Engineering exposure by enabling the DPU Engineering Mode in the appropriate Mode. (IC_ENBL_ENG) The ICU commands the detector to transmit the Engineering data to the Blue DSPs in the DPU.	<u>H7254</u> ENABLE ENG **H0880 "MODE" = FULL HI RES	
8.6	00:05:00	Wait up to approx. 300 s for the End of Exposure Alert DA_ENDOF_EXP. Alert indicates the end of the Exposure Task. At this point Blue DSP processing is disabled. (note: Alert DA_EOT_ENG is not nominally enabled) Packet 92205 (PK Dump 1149 OM4)		<u>AND: H910</u> H7675 "EXPOSURE NUMBER" = 2 H7680 "TIMESTAMP" = H7640 "FRAME TIME" = H7645 "FRAMES/EXPOSURE" =

Procedure. Ref.: SVT_OPM_5800 (1,1) HIGH VOLTAGE INTERFACE TEST

Step	Time	Activity/Remarks	Command	TM Verification
8.7		Stop Sending Events to DPU (Fail safe TC)	H6130 STOP DET INT	
8.8		Turn OFF Flood LEDs	H7134 SET FLOOD LED H0140 "LED CURRENT" = 0.000 UAMP	AND: H110 H5195 "FLOOD LED BIAS" = 0
9		RAMP DOWN HIGH VOLTAGES PRIME OR REDUNDANT.		
9.1		Specify the Vcathode ramp parameters for the ramp down - 0V	H7140 SET HV RAMP PARA **H0150 "VOLTAGE" = CATHODE **H0160 "TARGET VALUE" = 0 V **H0175 "FORCE RAMP" = OFF	
9.2		Perform Ramp. Monitor the ramp down in the Vcathode voltage to 0V.	H5140 START HV RAMP	AND: H100 H5165 "HVM VCATH" = 0 V
9.3	00:00:25	Wait up to 25 seconds for the confirmation of succesful ramp. Packet 92100 (PK Dump 1016 OM4)		AND: H911 HU700 "DETECTOR EVENT" = CATHODE OK
9.4		Specify the Vmcp1 ramp parameters for the ramp down - 0V	H7140 SET HV RAMP PARA **H0150 "VOLTAGE" = MCP1 **H0160 "TARGET VALUE" = 0 V **H0175 "FORCE RAMP" = OFF	
9.5		Perform Ramp. Monitor the ramp down in the VMCP1 voltage to 0V.	H5140 START HV RAMP	AND: H100 H5155 "HVM VMCP1" = 0 V
9.6	00:00:40	Wait up to 40 seconds for the confirmation of succesful ramp. Packet 92100 (PK Dump 1016 OM4)		AND: H911 HU700 "DETECTOR EVENT" = MCP1 OK
9.7		Specify the Vmcp23 ramp parameters for the ramp down - 0V	H7140 SET HV RAMP PARA **H0150 "VOLTAGE" = MCP23 **H0160 "TARGET VALUE" = 0 V **H0175 "FORCE RAMP" = OFF	
9.8		Perform Ramp. Monitor the ramp down in the VMCP 23 voltage to 0V.	H5140 START HV RAMP	AND: H100 H5160 "HVM VMCP23" = 0 V
9.9	00:00:20	Wait up to 20 seconds for the confirmation of succesful ramp. Packet 92100 (PK Dump 1016 OM4)		AND: H911 HU700 "DETECTOR EVENT" = MCP23 OK

Doc. Title.: XMM FOP
 Doc. Ref.: XMM-MOC-PL-0022-SMD
 Date: May 99

Issue : x
 Revision: x

Procedure. Ref.: SVT_OPM_5800 (1,1) HIGH VOLTAGE INTERFACE TEST

Step	Time	Activity/Remarks	Command	TM Verification
10	02:30:00	Await dumping of Engineering Image. Wait for event DA_SENT_CMPRSQ Packet 92217 (PK Dump 1162 OM4)		<u>AND: H911</u> H7650 "EXPOSURE ID" = 2 H7615 "DATATYPE" = H7655 "DATA SET ID" = H7745 "DATA IN ADDRESS" = H7660 "DATA IN LENGTH" = H7665 "DATA OUT ADDR0" = H7670 "DATA OUT LENGTH" = H7675 "EXPOSURE NUMBER" = H7680 "TIMESTAMP" =
11		Await complete exposure event. DA_COMPLETE_EXP Packet 92209 (PK Dump 1153 OM4)		<u>AND: H910</u> H7675 "EXPOSURE NUMBER" =2 H7680 "TIMESTAMP" =
12		Wait for telemetry dump to finish. DA_DATA_END Multiple Engineering data Packets 97322 (SOC Only). 92207 Data End event (PK Dump 1151 OM4) Indicates that the end of data has been reached.		<u>AND: H910</u> H7675 "EXPOSURE NUMBER" = 2 H7680 "TIMESTAMP" =
13		END OF PROCEDURE		