

Procedure. Ref.: SVT_OPM_5011 (1,1) INITIAL MODE PATCHES

Procedure Title: Initial Mode Patches

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Author: Kate Adamson

Purpose of Procedure:

This procedure executes the Initial Mode Patches using the RBI write commands.

Patches include the fix for the acceptance of commands without CRC in INITIAL Mode.

Refer to procedure : FCP_OPM_960x

This procedure is foreseen to be used during SVT.

Initial State: OM ON in INITIAL Mode

Final State: OM ON in INITIAL Mode

Step	Time	Activity/Remarks	Command	TM Verification
1		Confirm OM in INITIAL MODE		<u>AND: H100</u> H5395 "OM STATE" = INITIAL
2		UPLOAD INITIAL MODE PATCHES		
2.1		Disable Watchdog in preparation for RBI Suspend	<u>H6510</u> WDOG DISABLE	<u>AND: H100</u> H5345 "RBI CONFIG REG" = 0000 (OFF)
3		SET UP AND ENABLE PST2-10 This PST is the same as PST 1 but the OM Packet Terminal is replaced by RTU Table #16 - OM ICU Status. This allows monitoring of the RBI Status TM.		
3.1		Load RTU-Table 16 into buffer 1 Table 16 is the telemetry from the RBI Status Word for the OM packet Terminal.	<u>D6161</u> LOAD RTUT16	
3.2		Report preloaded 1st part of RTU-Table from buffer 1	<u>D6301</u> REPO LOAD 1	<u>AND: D510</u> D1998 "TM RTUTAB SID" = 16
3.3		Transfer Intermediate Buffer into RTU Table. Store 1st through 5th part of RTU-Table into operational RTU Table.	<u>D6300</u> STORE RTUTAB	
3.4		Load Parameters into PST2 for OM RBI. The RGS1 Packet Terminal is replaced by RTU Table number 16 which is the OM RBI Status	<u>D3021</u> LOAD SW OM	
3.5		Report PST 2. Confirm RTU table 16 has replaced all OM positions.	<u>D3097</u> REPO PST2	<u>AND: D520</u>
3.6		Change Polling Sequence Table to access the OM RBI status in RTU Table 16. INFORM OTHER USERS PST IS BEING CHANGED.	<u>D3099</u> PST2 MODE	<u>AND: D002</u> D5001 "PST MODE" = PST2 MODE D5002 "TM MODE" = TM MODE 2A

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4		Send RBI Suspend	<u>D7203</u> OM SUSPEND	<u>AND: D004</u> D6200 "OM RESET" = RESET D6201 "OM WAIT" = NOT WAIT D6202 "OM DMA BUSY" = DMA NOT BUSY D6203 "OM RUNNING" = NOT RUNNING
5		LOW LEVEL LOAD AND DUMP OF ICU MEMORY 1.		
5.1		Upload Initial Mode Non Volatile Patch using CDMU RBI Write TCs. Contents of LOAD: Address: 76E52 (hex) Data: E522 800D 0003 4A07 3000 7503 7200 0655 7070 1E0E	<u>D3920</u> MEMLOAD OM **C0042 "STRT ADDR OM" = 76E52 **CU100 "DATA WORD 0" = E522	
5.2		Upload Initial Mode Non Volatile Patch using CDMU RBI Write TCs.	<u>D3920</u> MEMLOAD OM **C0042 "STRT ADDR OM" = 76E53 **CU100 "DATA WORD 0" = 800D	
5.3		Upload Initial Mode Non Volatile Patch using CDMU RBI Write TCs.	<u>D3920</u> MEMLOAD OM **C0042 "STRT ADDR OM" = 76E54 **CU100 "DATA WORD 0" = 0003	
5.4		Upload Initial Mode Non Volatile Patch using CDMU RBI Write TCs.	<u>D3920</u> MEMLOAD OM **C0042 "STRT ADDR OM" = 76E55 **CU100 "DATA WORD 0" = 4A07	
5.5		Upload Initial Mode Non Volatile Patch using CDMU RBI Write TCs.	<u>D3920</u> MEMLOAD OM **C0042 "STRT ADDR OM" = 76E56 **CU100 "DATA WORD 0" = 3000	
5.6		Upload Initial Mode Non Volatile Patch using CDMU RBI Write TCs.	<u>D3920</u> MEMLOAD OM **C0042 "STRT ADDR OM" = 76E57 **CU100 "DATA WORD 0" = 7503	
5.7		Upload Initial Mode Non Volatile Patch using CDMU RBI Write TCs.	<u>D3920</u> MEMLOAD OM **C0042 "STRT ADDR OM" = 76E58 **CU100 "DATA WORD 0" = 7200	
5.8		Upload Initial Mode Non Volatile Patch using CDMU RBI Write TCs.	<u>D3920</u> MEMLOAD OM **C0042 "STRT ADDR OM" = 76E59 **CU100 "DATA WORD 0" = 0655	
5.9		Upload Initial Mode Non Volatile Patch using CDMU RBI Write TCs.	<u>D3920</u> MEMLOAD OM **C0042 "STRT ADDR OM" = 76E5A **CU100 "DATA WORD 0" = 7070	

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5.10		Upload Initial Mode Non Volatile Patch using CDMU RBI Write TCs.	D3920 MEMLOAD OM **C0042 "STRT ADDR OM" = 76E5B **CU100 "DATA WORD 0" = 1E0E	
5.11		Dump and compare Memory Patch. Memory Area to dump: Address 76E52 (hex) Length 10 (dec) TM Packet 2002 DS6	D3921 RBI DUMP OM **C0042 "STRT ADDR OM" = 76E52 **C0043 "LENGTH OM" = A	
6		LOW LEVEL LOAD AND DUMP OF ICU MEMORY 2.		
6.1		Upload Initial Mode Volatile Patch using CDMU RBI Write TCs. Contents of LOAD: Address: 73C18 (hex) Data: 7070 3729	D3920 MEMLOAD OM **C0042 "STRT ADDR OM" = 73C18 **CU100 "DATA WORD 0" = 7070	
6.2		Upload Initial Mode Volatile Patch using CDMU RBI Write TCs.	D3920 MEMLOAD OM **C0042 "STRT ADDR OM" = 73C19 **CU100 "DATA WORD 0" = 3729	
6.3		Dump and compare Memory Patch. Memory Area to dump: Address 73C18 (hex) Length 2 (dec) TM Packet 2002 DS6	D3921 RBI DUMP OM **C0042 "STRT ADDR OM" = 73C18 **C0043 "LENGTH OM" = 2	
7		Send RBI GO Command	D7204 OM GO	AND: D004 D6200 "OM RESET" = RESET D6201 "OM WAIT" = WAIT D6202 "OM DMA BUSY" = DMA BUSY D6203 "OM RUNNING" = RUNNING
8		Return to Nominal Polling Sequence Table. INFORM OTHER USERS PST IS BEING CHANGED.	D3098 PST1 MODE	AND: D002 D5001 "PST MODE" = PST1 MODE D5002 "TM MODE" = TM MODE 2A
9		Re-enable Watchdog. Check TM packet 91100 TC Acceptance packet is generated. (PK Dump 3100 OM3)	H5510 ENABLE WATCHDOG	AND: H100 H5345 "RBI CONFIG REG" = 0001 (ON) H5385 "TC PACKET COUNT" = should increment by 1
10		END OF PROCEDURE		