## 6.1 Input to Mentor

Files are generated from a Viewlogic conversion utility to mentor format.

## 6.3 Feedback from Mentor

The final net list files after completion of the layout should be saved here:

ftp://128.40.70.7/Public/Mentor/BPE/LOOKUP as "EM_Mentor_Net" and "EM_Mentor_Comp"

| | |
|---|---|
| Machine File-System Directory: | \\MSSLi7\data\projects\XMM-OM/MSSL/DD/0024.01 |
| Document File Name: | LOOKUP.DOC |
| Application: | MS Word 6 |
| Included files: | |
| Print Date (this): | 22-Oct-97 @ 12:23 |
| Printer: | Postscript |

# 1. Schematics

Drawing Number: 5229/032 Issue 3 4-10-97

Hierarchical list of sheets:

LOOKUP 1

LOOKUP 2

LOOKUP 3

LOOKUP 4

LOOKUP 5

LOOKUP 6

# 2. PCB Manufacturing and Board Assembly

| | | | |
|---|---|---|---|
| Drawing Number: | 5229/032-5 | 17-10-96 | drill plot |
| | 5229/032-6 | 17-10-96 | assembly drawing and parts list |

Artwork: X 1545-002

Serial Number:

Hypertac position: A1

# 3. Bill of Materials

Database printout for BPE-LOOKUP 21-10-97

# 4. FM Assembly List

Assembly list can be found on assembly drawing above

# 5. Modifications

1. Remove T1, T2, R1, R2.
2. Using appropriate holes made available in step 1 add inductor and capacitors as per sheet 2 of schematic.
3. Fit SOT 200 R pot to between hypertac pin 95 and PCLK signal. This sot will then be replaced with appropriate fixed value resister.

# 6. Layout Instructions

Use 50 tracks from hypertac +5V pins to current limit transistors. Net VBPE

# XMM Optical Monitor

MULLARD SPACE SCIENCE LABORATORY
UNIVERSITY COLLEGE LONDON Author: Duncan Self

## MSSL *XMM*OM QM BPE Lookup Design Definition

DOCUMENT: XMM-OM/MSSL/DD/0024.01

Distribution:

| | | |
|---|---|---|
| XMM-OM Project Office | R Hunt | orig |
| ESA PX | H Eggel | |
| CSL | P Rochus | |
| | S. Roose | |
| UCSB | T Sasseen | |
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| | K Mason | |
| | A Smith | |

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|---|---|---|---|
| Author: | D G Self | Date: | |
| OM Project Office | | Date: | |
| Distributed: | | Date: | |