1. Fit SOT 200 R pot to between hypertac pin 95 and PCLK signal. This sot will then be replaced with appropriate fixed value resister.
2. Polarity of C1 and C" is incorrect. Reverse each capacitor.

6. Layout Instructions

Use 50 tracks from hypertac +5V pins filter network.

Place track to a via on spare gates (sheet 12) before going to gnd for possible re-work.

See schmatic for de-coupling postion.

6.1 Input to Mentor

Files are generated from a Viewlogic conversion utility to mentor format.

6.2 Feedback from Mentor

The final netslist files after completion of the layout should be saved here:

ftp://128.40.70.7/Public/Mentor/BPE/DARRAY as "EM_Mentor_Net" and "EM_Mentor_Comp"

File-System Directory:	\\mssli7\data\projects\XMM-OM/MSSL/DD/0036.01
Document File Name:	FMdarray
Application:	MS Word 2
Included files:	
Print Date (this):	22-Oct-97 @ 12:16
Printer:	Postscript

1. Schematics

Drawing Number: 5229/039 Issue 3 4-8-97 14:22

Hierarchical list of sheets:

DARRAY 1

DARRAY 2

DARRAY 3

DARRAY 4

DARRAY 5

DARRAY 6

DARRAY 7

DARRAY 8

DARRAY 9

DARRAY 10

DARRAY 11

DARRAY 12

2. PCB Manufacturing and Board Assembly

Drawing Number: 5229/039-5 Issue 3 25-4-97 drill plot

5229/039-6 Issue 3 25-4-97 assembly drawing and parts list

Artwork: 1534 - 003

Serial Number:

Hypertac position D4

3. Bill of Materials

Database printout for BPE-DARRAY 20-10-97

4. QM Assembly List

Data base printout for BPE-DARRAY 21-10-97

5. Modifications

XMM Optical Monitor

MULLARD SPACE SCIENCE LABORATORY
UNIVERSITY COLLEGE LONDON Author: Duncan Self

MSSL *XMM*OM FM BPE Darray Design Definition

DOCUMENT: XMM-OM/MSSL/DD/0036.01

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