U27 pin5 VDD

U27 pin3 VDD

U27 pin14 GND

U27 pin12 VDD

U24 pin5 GND

U24 pin3 GND

U24 pin14 GND

U24 pin12 GND

U23 pin5 VDD

U23 pin3 VDD

U23 pin14 VDD

U23 pin12 VDD

3. Bill of Materials

Database printout for: BPE-SELECTOR 21-10-97

4. FM Assembly List

Data Base printout for: BPE-SELCETOR 21-10-97

5. Modifications

1. Do not fit T1, T2, R1, R2
2. Using appropriate holes and tracking made available in step 1 add inductor and capacitors as per sheet 2 of schematic.
3. Fit SOT 200 R pot between hypertac pin 95 and PCLK signal. This sot will then be replaced with appropriate fixed value resister.
4. ADD wire link between pin 6 and 8 of U55

6. Layout Instructions

Use 50 thou tracks from hypertac +5V pins to current limit transistors. Net VBPE.

6.1 Input to Mentor

Files are generated from a Viewlogic conversion utility to mentor format.

6.2 Notes

6.3 Feedback from Mentor

The final netslist files after completion of the layout should be saved here:

ftp://128.40.70.7/Public/Mentor/BPE/SELECTOR as "EM_Mentor_Net" and "EM_Mentor_Comp"

Machine File-System Directory:	\\MSSLi7\data\projects\XMM-OM/MSSL/DD/0038.01
Document File Name:	SELECTOR.DOC
Application:	MS Word 6
Included files:	
Print Date (this):	22-Oct-97 @ 12:18
Printer:	Postscript

1. Schematics

Drawing Number: 5229/029 4-8-1996 Issue 3

Hierarchical list of sheets:

SELECTOR 1

SELECTOR 2

SELECTOR 3

SELECTOR 4

SELECTOR 5

SELECTOR 6

SELECTOR 7

SELECTOR 8

SELECTOR 9

SELECTOR 10

SELECTOR 11

SELECTOR 12

2. PCB Manufacturing and Board Assembly

Drawing Number: 5229/029-5 28-11-96 drill plot

5229/029-6 28-11-96 assembly drawing and parts list

Artwork: 1543-003

Serial Number:

Hypertac Position: B2

ADD Wires

Add the following links to VDD or GND as specified. Due to a layout error not all points have a VDD or GND pad adjacent to them. Test first using a DVM.

U28 pin5 GND

U28 pin3 VDD

U28 pin14 GND

U28 pin12 VDD

XMM Optical Monitor

MULLARD SPACE SCIENCE LABORATORY
UNIVERSITY COLLEGE LONDON Author: Duncan Self

MSSL *XMM*OM FM BPE SELECTOR Design Definition

DOCUMENT: XMM-OM/MSSL/DD/0038.01

Distribution:

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